

1/5

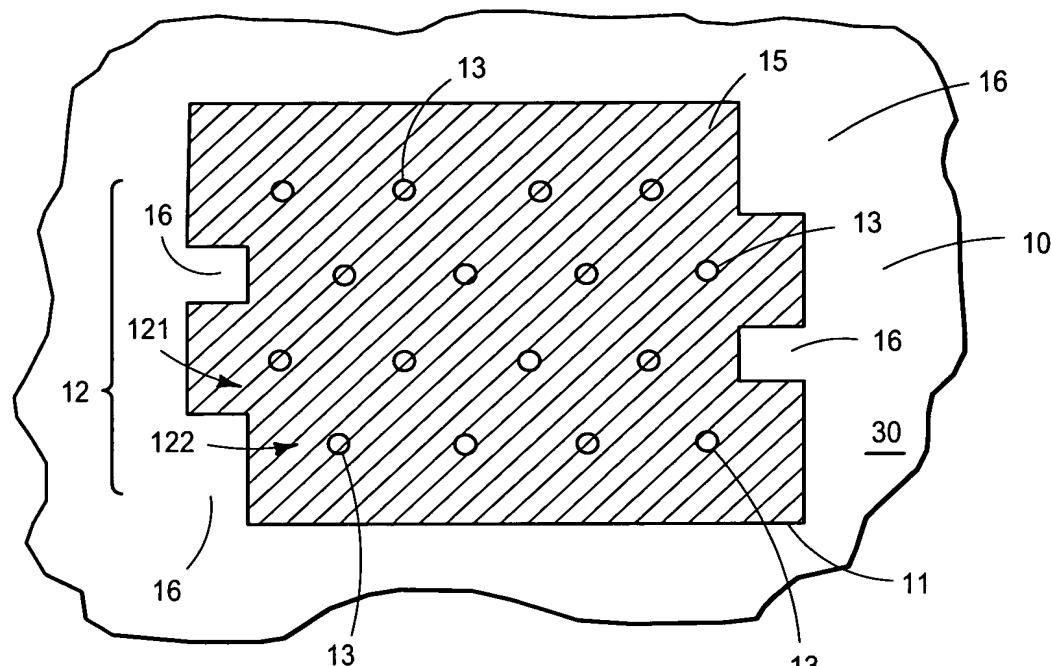


FIG. 1

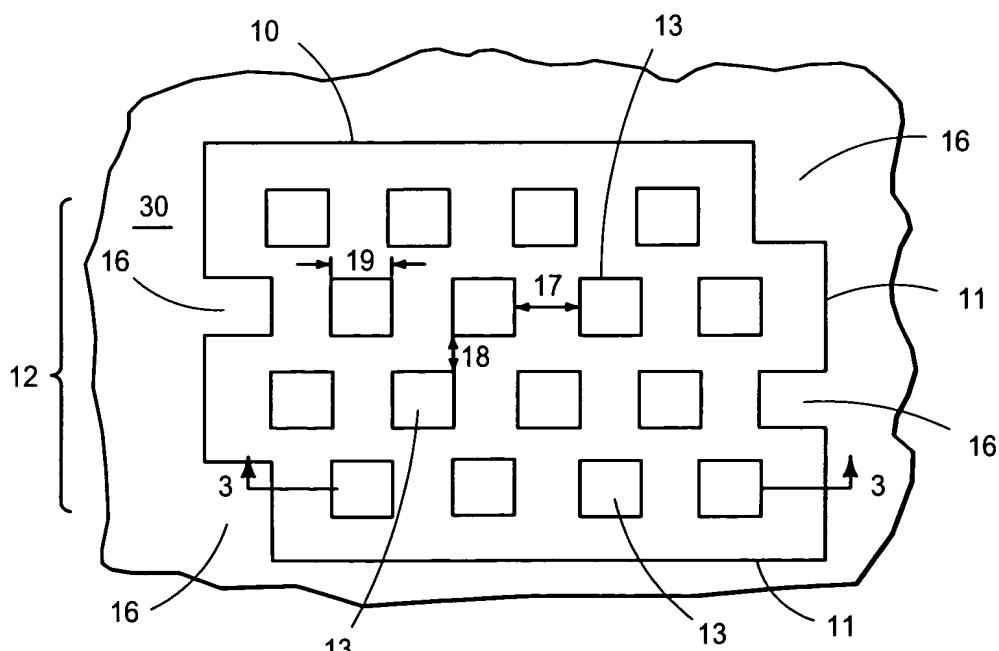


FIG. 2

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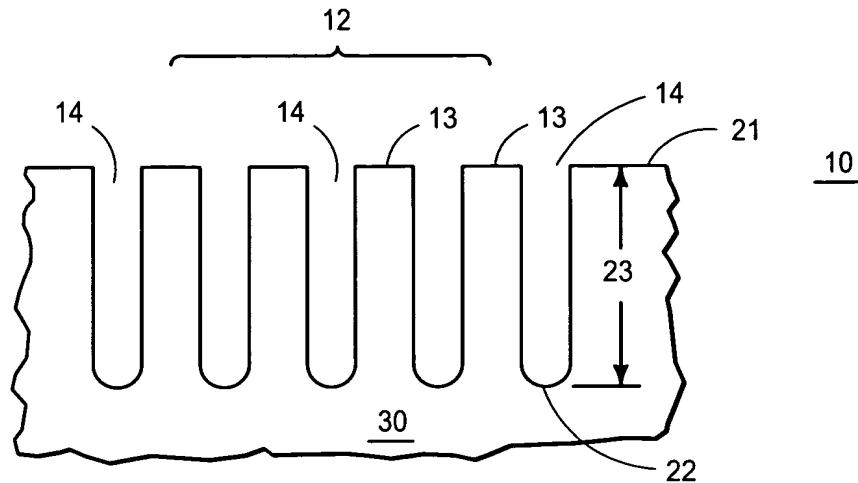


FIG. 3

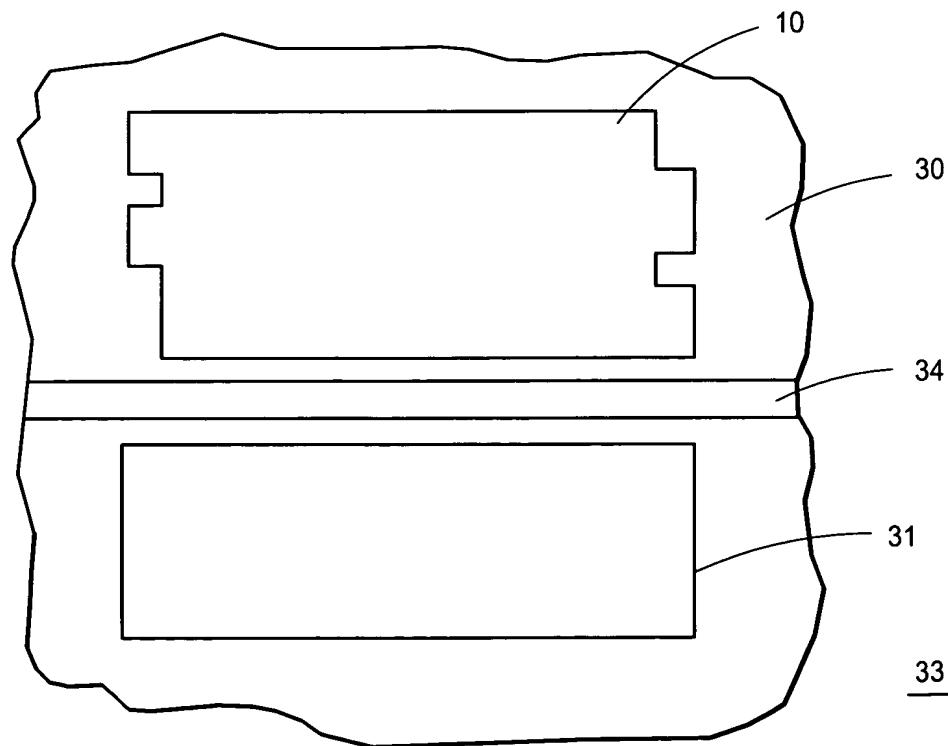


FIG. 4

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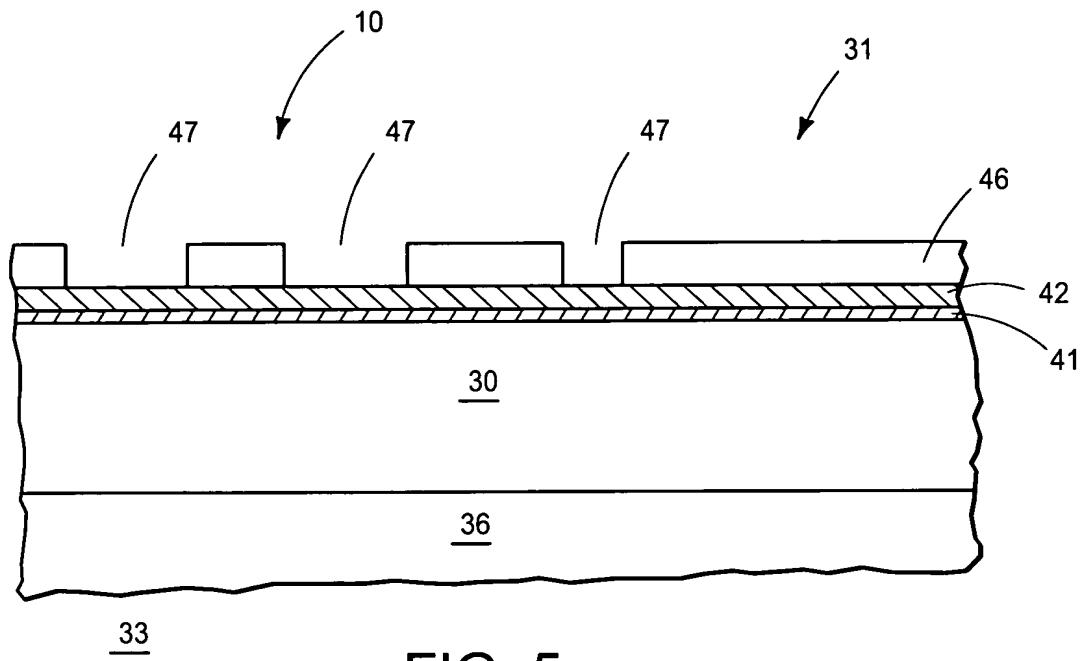


FIG. 5

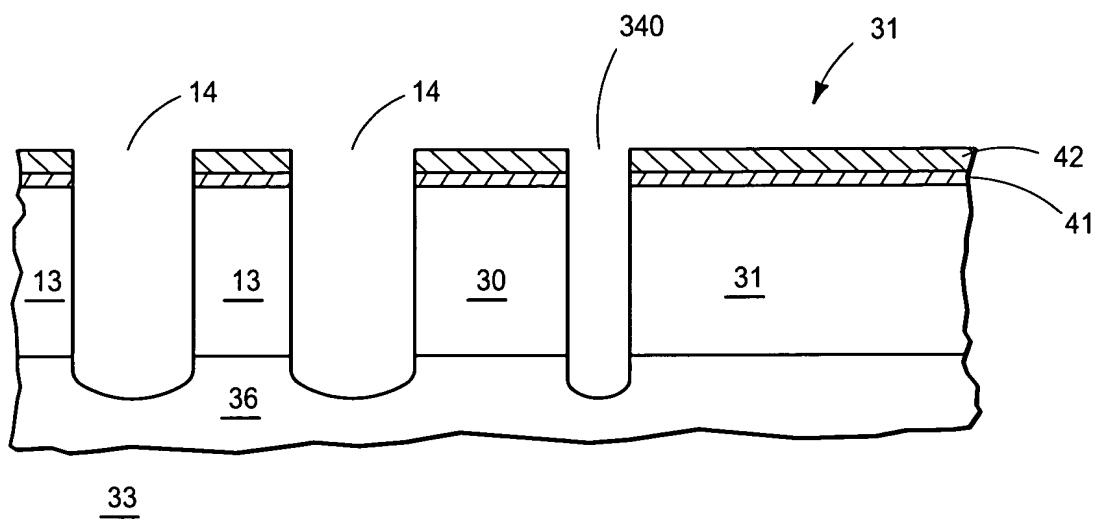


FIG. 6

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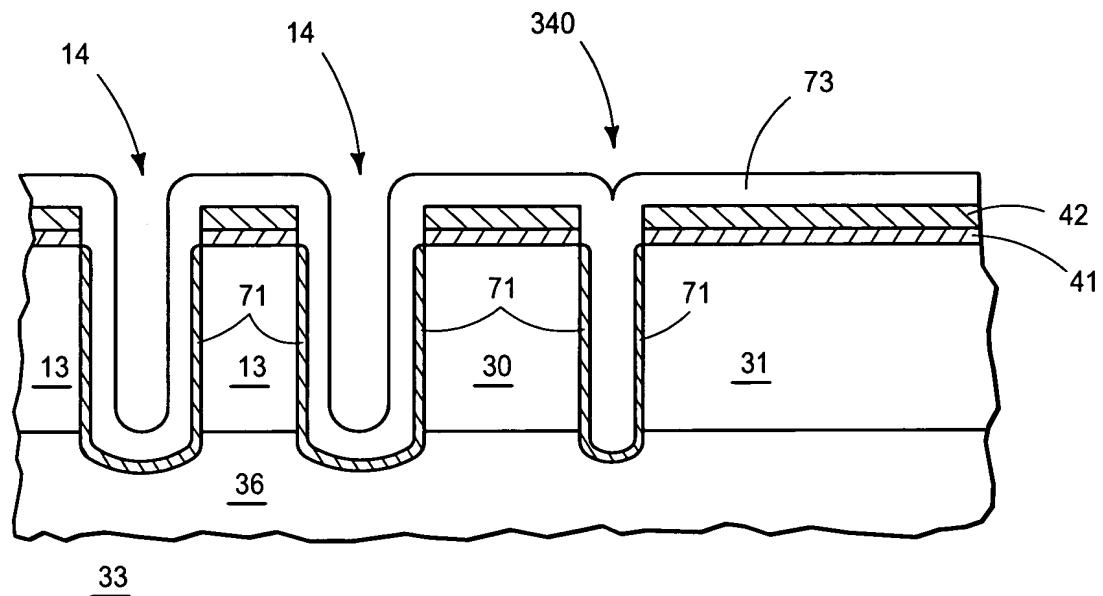


FIG. 7

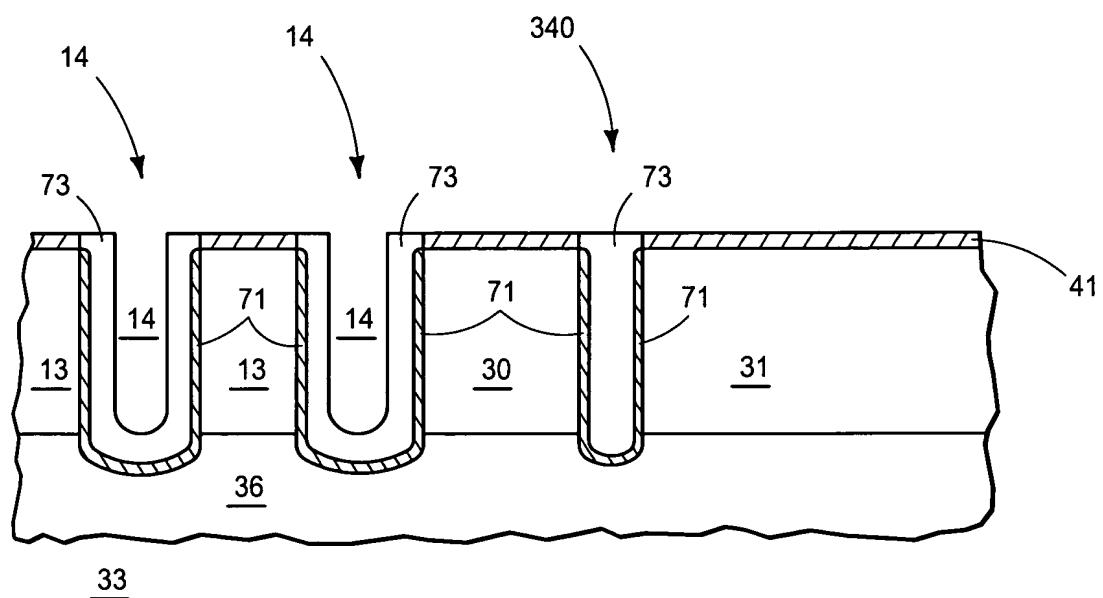


FIG. 8

**TITLE: SEMICONDUCTOR DEVICE HAVING REDUCED CAPACITANCE
TO SUBSTRATE AND METHOD**
INVENTOR: Gordon Grivna
DOCKET NO.: ONS00470

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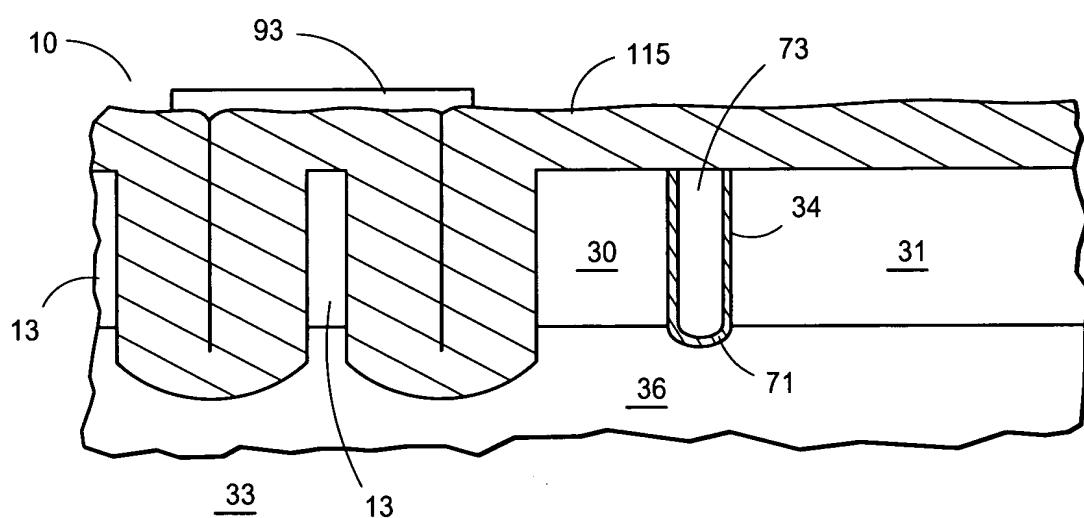


FIG. 9